

O.E.W.

①

1.28.

Two approaches

① If you want to prove that a system has a certain property you must prove it in general for all inputs.

② If you want to prove that it doesn't have a property provide a counter example.

If your initial guess is that property is not true and so you start to work on a counter example.

If you have trouble with counterexample and you start to believe that system has that property you will have to switch tracks and start working on the general statement in approach ①

Note: In using approach ① it will be convenient to call a system T .

so in (a) part $y[n] = x[-n]$

we may write $y[n] = T\{x[n]\} = x[-n]$.

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$$a) \quad y[n] = x[-n]$$

discrete time
system (square
bracket convention).

1) Memoryless.

try counter example

Examine

$$y[1] = x[-1]$$

Clearly output at time $t=1$ depends
on input at another time ($t=-1$)

\therefore NOT MEMORYLESS

2) Time Inv.

try

counter ex.

define

$$x_1[n] = \delta[n]$$

$$\therefore y_1[n] = \delta[-n] = \delta[n]$$

Now

$$x_2[n] = x[n-4]$$

← delayed
by 4.

$$= \delta[n-4]$$

$$y_2[n] = \delta[-n-4] = \delta[n+4]$$

delaying input by four
caused output to be advanced by 4.

\therefore NOT T.I

3) Linear (suspect true)

$$x_1[n] \xrightarrow{T} y_1[n]$$

$$x_2[n] \xrightarrow{T} y_2[n]$$

Examine $T\{x_1[n] + x_2[n]\}$

$$= x_1[-n] + x_2[-n]$$

$$= T\{x_1[n]\} + T\{x_2[n]\}$$

\therefore additivity holds.

Check.

$$T\{a x_1[n]\} = a x_1[-n] = a T\{x_1[n]\}$$

$a \in \mathbb{C}$

\therefore scalability holds

\therefore system is linear

4) Causal

Counter example

$$x_1[n] = \delta[n-1]$$

← impulse at 1

$$y_1[n] = T\{x_1[n]\} = \delta[-n+1] = \delta[n+1]$$

— impulse at -1

\therefore output at $t = -1$ depends on input at time 1

\therefore NOT ~~the~~ causal

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(4)

s) Stable.

given $x[n]$ s.t. $\exists M_x < \infty$
 $M_x \in \mathbb{R}$

s.t. $|x[n]| < M_x \quad \forall n \in \mathbb{Z}$

i.e. input $x[n]$ bounded.

Output is. $|y[n]| = |T\{x[n]\}| = |x[-n]| < M_x$

Clearly $y[n] < M_x \quad \forall n \in \mathbb{Z}$

\therefore output is bounded.

\therefore system is BIBO stable.

d) $y[n] = x[n-2] - 2x[n-8]$

i). Memoryless

$$x[n] = \delta[n]$$

$$y[n] = T\{x[n]\} = \delta[n-2] - 2\delta[n-8]$$

Clearly output at time $z \neq 8$
depends on input at time 0

\therefore NOT Memoryless

2)

T.I.

$$y[n] = T\{x[n]\}$$

Example

$$\begin{aligned} & T\{x[n-n_0]\} \\ &= x[n-n_0-2] - 2x[n-n_0-8] \\ &= x[n-2-n_0] - 2x[n-8-n_0] \\ &= y[n-n_0] \end{aligned}$$

\therefore system is T.I.

3) linear.

$$x_1[n] \xrightarrow{T} y_1[n]$$

$$x_2[n] \xrightarrow{T} y_2[n]$$

Check Additivity.

$$\begin{aligned} & T\{x_1[n] + x_2[n]\} = \\ &= x_1[n-2] + x_2[n-2] - 2x_1[n-8] - 2x_2[n-8] \\ &= x_1[n-2] - 2x_1[n-8] + x_2[n-2] - 2x_2[n-8] \\ &= T\{x_1[n]\} + T\{x_2[n]\} \end{aligned}$$

\therefore Additivity holds

$$0 \in \mathbb{W} \quad 1-28$$

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Scalability

$$\begin{aligned} \text{Check } T\{ax_1[n]\} & \quad a \in \mathbb{C} \\ &= ax_1[n-2] - 2ax_1[n-8] \\ &= a(x_1[n-2] - 2x_1[n-8]) \\ &= aT\{x_1[n]\} \end{aligned}$$

\therefore scalability hold.

\therefore system linear.

4) Causal.

$$\text{Now } y[n] = x[n-2] - 2x[n-8]$$

Clearly $\forall n \in \mathbb{Z}$ the output at n depends on input at $n-2$ and $n-8$ all in the past.

\therefore system is causal

5) Stable.

Assume $\exists M_x < \infty$ st.

$$|x[n]| < M_x \quad \forall n \in \mathbb{Z}$$

$$\begin{aligned} \text{Now } |y[n]| &= |x[n-2] - 2x[n-8]| \\ &\leq |x[n-2]| + 2|x[n-8]| \end{aligned}$$

Δ inequality

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$$|y[n]| < Mx + 2Mx = 3Mx < \infty$$

$\therefore y[n]$ is bounded with bound $3Mx$

\therefore system is BIBO stable.

d) $y[n] = n x[n]$.

1) Memoryless.

Clearly for $\forall n \in \mathbb{Z}$ output at time n depends only on input at time n .

\therefore Memoryless

2) T.I.

(counter example.)

Let $x_1[n] = \delta[n]$
 then $y_1[n] = T\{x_1[n]\} = n \delta[n] = 0$

Now $x_2[n] = x_1[n-1] = \delta[n-1]$

$$y_2[n] = T\{x_2[n]\} = T\{\delta[n-1]\} \\ = n \delta[n-1]$$

= 1

\therefore delaying input by 1 resulted in output which was not just a delay of original output

NOT T.I

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3) Linear.

$$x_1[n] \xrightarrow{T} y_1[n]$$

$$x_2[n] \xrightarrow{T} y_2[n]$$

Additivity
Check

$$\begin{aligned} & T\{x_1[n] + x_2[n]\} \\ &= n(x_1[n] + x_2[n]) \\ &= nx_1[n] + nx_2[n] \\ &= T\{x_1[n]\} + T\{x_2[n]\} \end{aligned}$$

\therefore additivity works

Scalability
Check

$$\begin{aligned} & T\{ax_1[n]\} \quad a \in \mathbb{C} \\ &= anx_1[n] \\ &= aT\{x_1[n]\} \end{aligned}$$

\therefore scalability works

\therefore system is linear.

4) Causal

Memoryless \Rightarrow Causal

\therefore system is causal

s) Stable.

Counter example.

$$\text{Let } x_1[n] = 1 \quad \forall n.$$

Clearly $x_1[n]$ is bounded.
for example 2 is a bound

$$T\{x_1[n]\} = n x_1[n] = n.$$

which grows without limit
as $n \uparrow$

\therefore output not bounded

\therefore NOT BIBO stable.

d) $y[n] = \mathcal{E}\{x[n-1]\}$

i) Memoryless

Counter example

$$x_1[n] = \delta[n]$$

$$\begin{aligned} T\{x_1[n]\} &= \mathcal{E}\{\delta[n-1]\} \\ &= \frac{1}{2} (\delta[n-1] + \delta[n+1]) \end{aligned}$$

\therefore output at time $t = -1$ depends
on input at time $t = 1$

\therefore NOT Memoryless

out
of
order

4) Causal
use above counter example again

since output at time $t = -1$
depends on input at $t = 0$ (future)

System is NOT causal

3) linear.

$$x_1[n] \xrightarrow{T} y_1[n]$$

$$x_2[n] \xrightarrow{T} y_2[n]$$

Additivity
check.

$$T\{x_1[n] + x_2[n]\}$$

$$= \mathcal{E}_v\{x_1[n-1] + x_2[n-1]\}$$

$$= \frac{1}{2} [x_1[n-1] + x_2[n-1] + x_1[-n-1] + x_2[-n-1]]$$

$$= \frac{1}{2} (x_1[n-1] + x_1[-n-1]) + \frac{1}{2} (x_2[n-1] + x_2[-n-1])$$

$$= \mathcal{E}_v\{x_1[n-1]\} + \mathcal{E}_v\{x_2[n-1]\}$$

$$= T\{x_1[n]\} + T\{x_2[n]\}$$

\therefore additivity holds

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Scaling.
Check

$$\begin{aligned}
 & T\{a x_1[n]\} && a \in \mathbb{C} \\
 &= \mathcal{E}\{a x_1[n-1]\} \\
 &= \frac{1}{2} (a x_1[n-1] + a x_1[-n-1]) \\
 &= \frac{1}{2} a (x_1[n-1] + x_1[-n-1]) \\
 &= a \mathcal{E}\{x_1[n-1]\} \\
 &= a T\{x_1[n]\}.
 \end{aligned}$$

 \therefore scaling holds \therefore linear holds2.) Periodic T.I.

Use

We imagine that TI was 4
hold since. output must
always be even. but the
delay of an even function is
not even

Counter ex.

$$x_1[n] = \delta[n]$$

$$T\{x_1[n]\} = \mathcal{E}\{\delta[n-1]\} = \frac{1}{2} (\delta[n-1] + \delta[-n-1])$$

$$\text{Check } T\{x_1[n-1]\} = T\{\delta[n-1]\}$$

$$= \mathcal{E}\{\delta[n-2]\} = \frac{1}{2} (\delta[n-2] + \delta[-n-2])$$

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This is not just a delayed version of original output.

NOT TI.

5) Stable.

Assume $\exists M_x < \infty$ s.t.

$$|x[n]| < M_x \quad \forall n \in \mathbb{Z}$$

Examine

$$|y[n]| = |T\{x[n]\}|$$

$$= |e^{j\omega} \{x[n-1]\}|$$

$$= \left| \frac{1}{2} [x[n-1] + x[-n-1]] \right|$$

$$\leq \frac{1}{2} |x[n-1]| + \frac{1}{2} |x[-n-1]| \quad (\Delta \text{ing.})$$

$$< \frac{1}{2} M_x + \frac{1}{2} M_x$$

$$= M_x$$

\therefore output is bdd. with same bound as input system is stable

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(B)

1)

$$y[n] = \begin{cases} x[n], & n \geq 1 \\ 0, & n = 0 \\ x[n+1], & n \leq -1 \end{cases}$$

clips out input signal at time $t=0$ moves left side further left.

1). Memoryless.

Counter example.

$$x_1[n] = \delta[n+1]$$

$$y_1[n] = T\{x_1[n]\} = \delta[n+2]$$

Clearly output at time -2 depends on input at time -1 .

\therefore NOT Memoryless.

2) T.I.

counter ex.

$$x_1[n] = \delta[n]$$

$$y_1[n] = T\{x_1[n]\} = \delta[n+1]$$

consider. $x_1[n-1] = \delta[n-1]$

$$T\{x_1[n-1]\} = T\{\delta[n-1]\} = \delta[n-1]$$

not just a delayed version of orig output

\therefore not T.I.

3) linear.

Examine

$$T\{x_1[n] + x_2[n]\}.$$

$$= \begin{cases} x_1[n] + x_2[n] & n \geq 1 \\ 0 & n = 0 \\ x_1[n+1] + x_2[n] & n \leq -1. \end{cases}$$

$$= \begin{cases} x_1[n] & n \geq 1 \\ 0 & n = 0 \\ x_1[n+1] & n \leq -1 \end{cases} + \begin{cases} x_2[n] & n \geq 1 \\ 0 & n = 0 \\ x_2[n+1] & n \leq -1 \end{cases}$$

$$= T\{x_1[n]\} + T\{x_2[n]\}.$$

\therefore additivity holds.

Scaling

Examine

$$T\{ax_1[n]\}$$

$$a \in \mathbb{C}$$

$$= \begin{cases} ax_1[n] & n \geq 1 \\ 0 & n = 0 \\ ax_1[n+1] & n \leq -1 \end{cases} = a \begin{cases} x_1[n] & n \geq 1 \\ 0 & n = 0 \\ x_1[n+1] & n \leq -1 \end{cases}$$

$$= a T\{x_1[n]\}$$

∴ scaling holds

∴ system linear.

4) Causal

Using counter example.

$$x_1[n] = \delta[n]$$

$$y_1[n] = T\{x_1[n]\} = \delta[n+1]$$

∴ output at $t=1$ depends on input at $t=0$ (future)

∴ not causal.

5) Stable

Assume $\exists M_x < \infty$ s.t.

$$|x[n]| < M_x \quad \forall n \in \mathbb{Z}$$

Examine.

$$|y[n]| = \begin{cases} x[n] & n \geq 1 \\ 0 & n = 0 \\ x[n+1] & n \leq -1 \end{cases}$$

$$= \begin{cases} |x[n]| & n \geq 1 \\ 0 & n = 0 \\ |x[n+1]| & n \leq -1 \end{cases} = \begin{cases} M_x & n \geq 1 \\ 0 & n = 0 \\ M_x & n \leq -1 \end{cases}$$

$$|y[n]| < Mx.$$

$\therefore y[n]$ is bdd by same bound as $x[n]$

\therefore system is stable.

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1.30 a). $y(t) = x(t-4)$

clearly ^{delay input by 4} inverse operation is to advance by 4.

So using



(use this diagram for future parts of this question).

then.

T_2 ^(inverse) is

$$w(t) = y(t+4)$$

b) $y(t) = \cos(x(t))$

Not invertible.

let $x_1(t) = 0$ constant $\forall t \in \mathbb{R}$

$$x_2(t) = 2\pi$$

$$y_1(t) = T_1\{x_1(t)\} = 1$$

$$y_2(t) = T_1\{x_2(t)\} = 1$$

Same output diff. inputs
 \therefore not invertible.

$O \xi' u$

1-30

(2)

$$c) \quad y[n] = n \times [a].$$

↳ only problem pt. here is $n=0$

consider $x_1[n] = \delta[n]$

$$x_2[n] = 2\delta[n].$$

$$y_1[n] = T_1\{x_1[n]\} = 0$$

$$y_2[n] = T_1\{x_2[n]\} = 0.$$

∴ two diff inputs have same output
 ∴ not 1-1
 ∴ not invertible.

$$d) \quad y(t) = \int_{-\infty}^t x(\tau) d\tau.$$

Recall calculus formula for diff.
 integral functions

$$\frac{d}{du} \int_{\psi_1(u)}^{\psi_2(u)} f(u, v) dv.$$

$$= \int_{\psi_1(u)}^{\psi_2(u)} f_u(u, v) dv - \psi_1'(u) f(u, \psi_1(u)) \\ + \psi_2'(u) f(u, \psi_2(u))$$

where prime is deriv. wrt u .
 and f_u is partial deriv. wrt u .

Using this formula. we note:

$$\frac{d}{dt} y(t) = \frac{d}{dt} \int_{-\infty}^t x(\tau) d\tau.$$

$$= x(t).$$

Unsurprisingly since integrating then diff. should yield same result.

\therefore inverse system T_2 is

$$w(t) = \frac{d}{dt} y(t).$$

$$e) \quad y[n] = \begin{cases} \int x[n-1] & n \geq 1 \\ 0 & n = 0 \\ x[n] & n \leq -1 \end{cases}$$

take $x[n]$ for $n \geq 0$ delay by 1
leave rest alone.
opens hole at $n = 0$

Clearly inverse system requires only that we move data back

invert system

1. 3a,
0, w

(4)

$$w[n] = \begin{cases} y[n+1], & n \geq 0 \\ y[n], & n < 0 \end{cases}$$

b) $y[n] = x[n] x[n-1]$.

$$x_1[n] = 0 \Rightarrow y_1[n] = T_1\{x_1[n]\} = 0$$

$$x_2[n] = \delta[n] \Rightarrow y_2[n] = T_2\{x_2[n]\} = 0$$

\therefore two inputs give same output.
 \therefore not 1-1.

\therefore not invertible.

1.28 d) Proof of time variance:

$$Y[n] = E_V \{x[n-1]\} = \frac{1}{2} x[n-1] + \frac{1}{2} x[-n-1]$$

$$x_1[n] \rightarrow Y_1[n] = \frac{1}{2} x_1[n-1] + \frac{1}{2} x_1[-n-1]$$

$$x_2[n] = x_1[n-n_0] \rightarrow Y_2[n] = \frac{1}{2} x_2[n-1] + \frac{1}{2} x_2[-n-1] \\ = \frac{1}{2} x_1[n-1-n_0] + \frac{1}{2} x_1[-n-1-n_0]$$

$$\rightarrow Y_1[n-n_0] = \frac{1}{2} x_1[n-1-n_0] + \frac{1}{2} x_1[-n-1+n_0]$$

therefor $Y_1[n-n_0] \neq Y_2[n]$ for all values of n
and system is time variant.

Note that this is another method, ~~also~~

See also the method in the solution of

Tutorial 3,