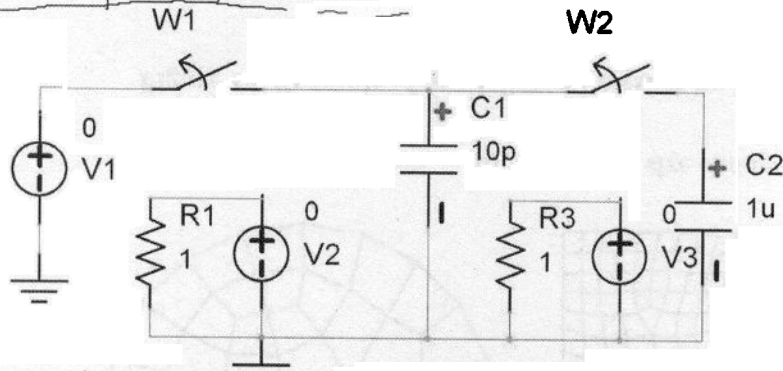


Lect#9, Filter Course

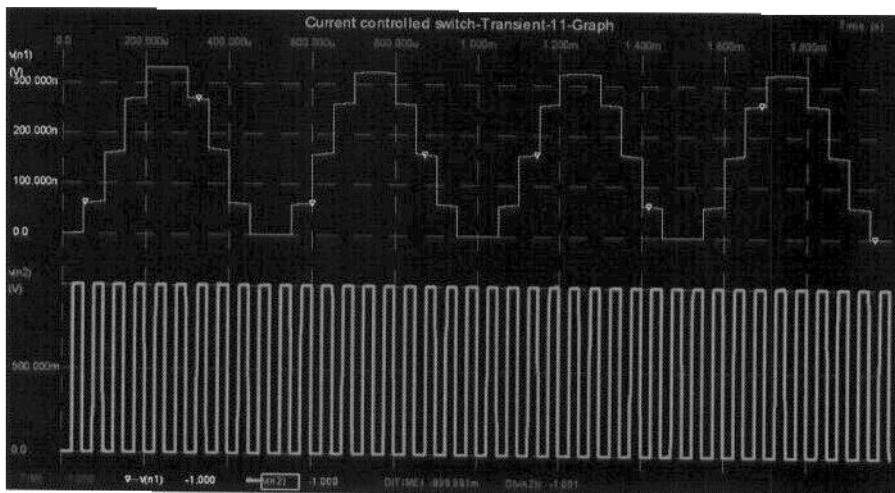
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Usage of Current Controlled Switch in B2 spice for Switched Cap. Filter

Demo. of a S/H circuit



V1 → signal source, sine wave $f = 2\text{kHz}$, $V_A = 20\text{mV}$ (say)
 W1 → current controlled switch, controlled by V_2
 W2 → - same - but controlled by V_3



$R_{ON} = 100\Omega$, $R_{OFF} = 100\text{M}\Omega$ for both W_1, W_2

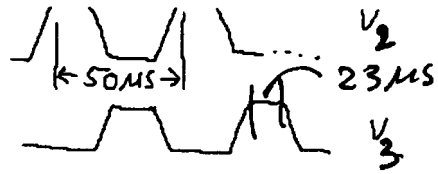
V_2, V_3 non overlapping clock signals

	V_2 (low = 0V)	V_3 (low = 0V)
V_A	1V	1V ← high voltage
F	0	25ns ← initial delay
T_r, F	1μs	1μs

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T_w	23μs	23μs ← width
T_p	50μs	50μs ← period





The graphs show the sample-and-held waveform across 1 uF capacitor, and one of the clock pulse waveforms