Experiment 1

Passive Filters (LCR based)

(Simulation)

Objective

To build highpass, lowpass and bandpass LCR filters[[1]](#footnote-1) using circuit simulation tools.

Introduction

Ladder networks are filters of the first kind, built in the early part of last century, before op-amp technology was available. The lossless, or reactance ladder, shown in Fig. 1, is still used today in applications where op-amp are not suited. For example, if the required power is beyond the capability of the op-amp, or for high frequency applications. The ladder circuit shown here is considered a prototype lowpass filter, and therefore frequency transformations may be applied to this structure to obtain different responses, and hence more complicated ladder structures.

The synthesis of a passive filter can be accomplished via many different methods. In this lab, you will design lowpass, highpass and bandpass lossless ladder filters using the frequency transformation techniques. The circuits will exhibit Butterworth and Chebyshev responses, in singly and doubly terminated configurations. The voltage transfer functions are all-pole functions and are characterized by:



The Lowpass Lossless Ladder

In the low-pass ladder network all the series elements are inductors and the shunt elements are capacitors. The synthesis for a singly terminated ladder network is outlined here.

Singly Terminated Networks

Given a singly terminated network with a transfer function which represents a Butterworth or Chebyshev response, we can express as a ratio of polynomials Q(s), M(s) and N(s).

, if the degree of M(s) > the degree N(s), or,

, if the degree of N(s) > the degree M(s)

Where M(s) = even portion of the denominator, and N(s) = odd portion of the denominator. If the degree of M(s) > degree of N(s), we synthesize Y22 = M(s) / N(s), and the first element of the ladder is a capacitor in shunt. Otherwise we synthesize Z22 = N(s) / M(s) and the first element is an inductor in the series ( where Y22 and Z22 are two-port network parameters). The polynomial division can be done by long division, the division algorithm are capable of performing polynomial division. The normalized design is performed for a 1 ohm load and a cut-off frequency of 1 rad/sec. Then the circuit is scaled accordingly. If the denominator has a degree n then there are a total of n L & C elements.

Example:

Synthesize the function;



As a singly terminated LC ladder network. By inspection,

 and 

Since the degree of M(s) > the degree of N(s) we synthesize Y22. Therefore,



The results of the division algorithm are;

|  |  |
| --- | --- |
| 0.383s | 1 3.414 1  |
| 1.082s | 2.613 2.613  |
| 1.577s | 2.414 1  |
| 1.531s | 1.531 |
|  | 1 |

Since M(s) > N(s) the first element in the ladder network is a capacitor in shunt, and the element values correspond to the results in the division algorithm. These elements are considered from the load looking toward the source (hence Z22 or Y22).



Doubly terminated Networks

The doubly terminated networks are similar to the singly terminated networks in their synthesis. We always design a normalized filter (1 ohm resistors and a cut-off frequency of 1 rad/sec). And then magnitude and frequency scale the components. With doubly terminated networks we synthesize Zin. If the order of the numerator is smaller than the order of the denominator, the network starts with a capacitor in shunt, otherwise if the order of the numerator is greater than the order of the denominator, the ladder starts with an inductor in series. The most mechanical process involved in these designs is obtaining Zin. This procedure will be covered in your lectures. We will work from the assumption that you are given Zin.



Given:



Polynomial division yields the following components, for the nominal low-pass transfer function.













Using a termination of  and  rad/sec,  and , and the circuit is shown in Fig. 5 . Note that

Km = Rf/Ri and Kf = ωf/ωi . Here the subscript f stands for the final value and the subscript I stands for the initial value to which the scaling is applied.

Frequency Transformations

The normalized, doubly-terminated, lowpass filter, (R1=R2=1 and  rad/sec ) can be transformed into a highpass or bandpass filter.

Highhpass Transformation

With the normalized circuit;

Replace the inductors by capacitors with the values 1/L.

Replace the capacitors by inductors with the values 1/C.

Once the transformation is done, magnitude and frequency scaling is applied.

Bandpass Transformation

 A bandpass filter can be realized by synthesizing a normal lowpass ladder circuit, and then appling the following transformations. Series inductors are replaced by a series combination of an inductorand a capacitor, with the values  and  as shown in Fig. 6.

 

Shunt capacitros are replaced by a parallel combination of a capacitor and an inductor, with the values  and  as shown in Fig. 7 . Here ωo is the center frequency of the band-pass filter.

 

Note that the bandwidth (BW) is calculated in Hz and the center-frequency (ωo) is calculated in rad/s.

Pre-Lab

1. Synthesize the following transfer function, of a singly terminated network, for a 1K load resistor. The normalized circuit operates at 1 rad/sec. Scale the circuit for a cut-off frequency of 10KHz. 
2. Synthesize the transfer function, of a doubly terminated network terminated in 100 ohm resistors, using Zin given below. Use the closest available capacitros. The normalized circuit operates at 1 rad/sec. Scale the cicuit for a cut-off frequencyof 8.6KHz. 
3. Convert the lowpass filter designed in part 2 of the prelab to a highpass filter using the frequency transformation techniques.
4. Convert the LPF in step 2 to a BPF with a bandwidth (BW) of 5KHz. Assume that the center frequency (ωo) for the BPF is 20 KHz (ωo=2π x 20x103 rad/s). All the resistors are 1 Ω.

Procedure

1. Simulate the circuit in part 1 of the pre-lab using sinusoidal signal with 1V peak-to-peak and frequency sweep from 100Hz to 30 KHz.
2. Repeat step 1 for the circuit in part 2 of the pre-lab.
3. Repeat step 1 for the circuit in part 3, and 4 of the pre-lab. Do the simulation.While simulating part 4 of pre-lab, it is better to sweep between 10 KHz and 50 KHz. In the simulation settings use 100 points per decade to get a smooth curve. Verify the functionality. In all these cases attach the circuits and the output with your lab report.
1. Please consult the text book , …. For your understanding. [↑](#footnote-ref-1)